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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hisashi Ohtani

Serial No.: 09/596,755

Art Unit : 1765

Examiner: Robert Kunemund

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DEL 0 9 50

Title :

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: June 15, 2000

: MANUFACTURING METHOD OF SEMICONDUCTOR AND MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Please direct all future official communications to Customer No. 26171.

Respectfully submitted,

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